

6. (Amended) A circuit arrangement as claimed in claim 4, characterized in that the capacitive element (14; 24; 34) is arranged at the ends of a single turn.

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7. (Amended) A circuit arrangement as claimed in claim 1, characterized in that at least two inductive elements (12; 22; 32) each comprise one, preferably a plurality of turns which are substantially concentric and/or substantially parallel to each other.

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8. (Amended) A circuit arrangement as claimed in claim 1, characterized in that the capacitive elements (14; 24; 34) are arranged one after the other in essentially one direction (D).

9. (Amended) A circuit arrangement as claimed in claim 1, characterized in that more than two resonant circuits (10; 20; 30) are magnetically fixedly coupled to each other.

10. (Amended) A circuit arrangement as claimed in claim 1, characterized in that the resonant circuits (10; 20; 30) of the circuit arrangement (100) are arranged on the upper metallization plate (40) of the integrated circuit.

11. (Amended) An integrated circuit comprising at least a circuit arrangement (100) as claimed in claim 1.

REMARKS

The foregoing amendments to claims 3-4 and 6-11, were made solely to avoid filing the claims in the multiple dependent form so as to avoid the additional filing fee.

The claims were not amended in order to address issues of patentability and Applicant respectfully reserves all